

Title: METHOD AND APPARATUS FOR MANAGING DATA CACHING IN A DISTRIBUTED
 Inventor: C. Mandal et al.
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- StorEdge Data Services Console: Solaris Management Console 2.0 EA

Console Exit View Go Help

Navigation

- StorEdge Data Service Console
- Data Service Stations
 - Station 0
 - Data Service Volumes
 - Local Volumes
 - Exploded Volumes
- Data Service Cache
- Cache Statistics
- Fast Write Cache
- Instant Image
- Network Data Replicator

Service Status OK — 622

Configuration

Threads: 626 628

Polling interval (sec): 128

Host Memory Cache Size (MB): 630 632

Block Size (kb): 650

Apply 634

System Cache Policy

Disable Cache 642

Enable Cache 644

Write 645

646 — First Write Cache not installed 652

Cache	Statistics	Statistic	Value
Read Hits Total		0	
Read Misses Total		0	
Write Hits Total		0	
Write Misses Total		0	
Write Blocks Available		Write Blocks Available	

Information

This tool allows you to manage your data service cache statistics.

Cache Statistics

Context Help

Console Log

0 item(s)

638

618

620

624

626

628

630

632

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648

FIG. 6

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- StorEdge Data Services Console: Solaris Management Console 2.0 EA																																																									
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http://downtown:8900/bones/DataServicesConsole/txtDataService Stations/Station/Data Service Cache/Fast Write Cache																																																									
<p>702</p> <p>704</p> <p>Navigation</p> <ul style="list-style-type: none"> 9 Data Service Stations 9 Station 0 9 Data Service Volumes 9 Local Volumes 9 Exported Volumes 9 Data Service Cache 9 Cache Statistics 9 Fast Write Cache 9 Data Service Application <p>720</p> <p>718</p> <p>738</p> <p>Information</p> <p></p> <p>This tool allows you to manage your Fast Write Cache data service</p> <p>Fast Write Cache</p> <p>Context Help</p> <p>Console Log</p> <p>0 Item(s)</p>																																																									
<p>Status: Nvram Cards are operating normally — 722</p> <table border="1"> <thead> <tr> <th colspan="2">Nvram 0</th> <th colspan="2">Nvram 1</th> </tr> <tr> <th>Properties</th> <th>Values</th> <th>Properties</th> <th>Values</th> </tr> </thead> <tbody> <tr> <td>Device ID</td> <td>NVRAM0</td> <td>Device ID</td> <td>NVRAM0</td> </tr> <tr> <td>Software Version</td> <td>2</td> <td>Software Version</td> <td>2</td> </tr> <tr> <td>Hardware Version</td> <td>0</td> <td>Hardware Version</td> <td>0</td> </tr> <tr> <td>Board Size</td> <td>32MB</td> <td>Board Size</td> <td>32MB</td> </tr> <tr> <td>Card Instance</td> <td>0</td> <td>Card Instance</td> <td>0</td> </tr> <tr> <td>Bus Instance</td> <td>pc#0</td> <td>Bus Instance</td> <td>pc#0</td> </tr> <tr> <td>Card Status</td> <td>Active</td> <td>Card Status</td> <td>Active</td> </tr> <tr> <td>Dirty Bit</td> <td>1</td> <td>Dirty Bit</td> <td>1</td> </tr> <tr> <td>Battery Status</td> <td>Good</td> <td>Battery Status</td> <td>Good</td> </tr> <tr> <td>Uncorrectable Errors</td> <td>0</td> <td>Uncorrectable Errors</td> <td>0</td> </tr> <tr> <td>Correctable Errors</td> <td>0</td> <td>Correctable Errors</td> <td>0</td> </tr> <tr> <td>Mirror ID</td> <td>NVRAM1</td> <td>Mirror ID</td> <td>NVRAM0</td> </tr> </tbody> </table>		Nvram 0		Nvram 1		Properties	Values	Properties	Values	Device ID	NVRAM0	Device ID	NVRAM0	Software Version	2	Software Version	2	Hardware Version	0	Hardware Version	0	Board Size	32MB	Board Size	32MB	Card Instance	0	Card Instance	0	Bus Instance	pc#0	Bus Instance	pc#0	Card Status	Active	Card Status	Active	Dirty Bit	1	Dirty Bit	1	Battery Status	Good	Battery Status	Good	Uncorrectable Errors	0	Uncorrectable Errors	0	Correctable Errors	0	Correctable Errors	0	Mirror ID	NVRAM1	Mirror ID	NVRAM0
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FIG. 7